## WHAT WE CLAIM IS:

1.	A proces	ss for transmitting	digital dat	a to an A	/D converter	via an a	nalog
channel,	comprising:						

generating a sequence of output signals by precoding a sequence of input signal points to precompensate for ISI in the analog channel, at least one of the output signals would be distorted by the ISI to a signal representative of a signal point lying between quantization levels of the A/D converter if the precoding matches the ISI, others of the output signals would be distorted by the ISI to signals representative of signal points that are quantization levels of the A/D converter if the precoding matches the ISI, a majority of the input signal points are quantization levels of the A/D converter.

- 2. The process of claim 1, wherein the quantization levels are the quantization levels of a PCM vocoder.
  - 3. The process of claim 2, wherein the precoding further comprises: precompensating an input signal point for the ISI; and adding a modulo amount to the precompensated signal point in response to precompensated signal point having a value outside of a threshold range.
- 4. The process of claim 3, wherein the adding includes determining the value of the modulo amount in a manner responsive to the value of the input signal point that was precompensated.
- 1 5. The process of claim 4, wherein the threshold range is dependent on the value of the input signal point that was precompensated.
- 1 6. The process of claim 4, wherein the threshold range is independent of the value of the input signal point that was precompensated.
- 7. The process of claim 1, wherein another of the output signals would be distorted by the ISI to a signal representative of a signal point lying between quantization

4

5

6

7

1

2

1

2

3

1

- 3 levels of the A/D converter, first and second ones of the output signals lying between
- 4 different pairs of quantization levels.
- 1 8. The process of claim 1, wherein each of the input signal points is 2 representative of a value of a quantization level of the D/C converter.
- 9. A transmitter for transmitting digital data to an A/D converter via an analog channel, the transmitter comprising:
  - a precoder being configured to precompensate a sequence of input signal points for ISI in the analog channel and being characterized by an original constellation and an extended constellation, a majority of the signal points of the original constellation being quantization levels of the A/D converter, and the extended constellation including at least one signal point that is not a quantization level of the A/D converter.
  - 10. The transmitter of claim 9, wherein the quantization levels characterizing a portion of the points in the constellations are quantization levels of a PCM vocoder.
  - 11. The transmitter of claim 10, further comprising:
    a trellis encoder to produce the input signal points by encoding digital data, the trellis encoder is coupled to transmit the input signal points to the precoder.
- 1 12. The transmitter of claim 11, wherein the trellis encoder is configured to 2 produce signal points representative of the quantization levels of the PCM vocoder.
- 1 13. The transmitter of claim 10, wherein the precoder trellis encodes the input signal points and further produces signals representative of signal points of the original constellation in response to being configured to precompensate for zero ISI in the analog channel.
  - 14. The transmitter of claim 10, wherein the precoder comprises:

3

4

5

6

1

2

1

2

3

a digital filter to generate feedback signal points from precompensated signal
points produced from the input signal points by the precoder;
an adder to combine the feedback and associated input signal points; and
a modulo device to produce a signal point within a threshold range from a signal
point received from the adder.

- 1 The transmitter of claim 14, wherein the modulo device is configured to 15. add a modulo amount to a signal point received from the adder in response to the 2 received signal point not having a value in the threshold range. 3
- 1 The transmitter of claim 14, wherein the threshold range is dependent on 16. 2 the value of the input signal point that was precompensated.
  - The transmitter of claim 14, wherein the threshold range is independent of 17. the value of the input signal point that was precompensated.
  - 18. The transmitter of claim 15, wherein the modulo device is configured to determine the threshold range from the value of the associated input signal point.
- The transmitter of claim 9, wherein the extended constellation includes a 1 19. plurality of signal point values that are not equal to quantization levels of the A/D 2 converter.
- 1 A process for communicating data over an analog channel in both 20. 2 downstream and upstream directions, comprising:
- 3 monitoring a quantity representative of an echo level caused by downstream data 4 transmission:
- 5 resetting a power level for downstream transmissions over the analog channel to a lower value in response to determining that the echo level interferes with upstream data 6 7 transmissions over the analog channel.

1	21. The process of claim 20, further comprising:					
2	channel encoding data prior to transmitting the data downstream; and					
3	decoding upstream transmitted data to recover the data.					
1	22. The process of claim 20, further comprising:					
2	resetting includes selecting a constellation for the downstream data transmission					
3	that produces a lower average power level on the analog channel.					
1	23. The process of claim 20, wherein the echo level changes the amount of					
2	quantization noise in upstream transmitted data.					
	* · · · · · · · · · · · · · · · · · · ·					
1	24. A transceiver for transmitting and receiving digital data via a mixed					
2	channel that includes a digital network and an analog channel serially connected to the					
3	digital network, comprising:					
4	a transmitter capable of error-encoding digital data and of transmitting encoded					
5	data downstream to a target transceiver via the mixed channel; and					
6	a receiver capable of error-decoding received digital data transmitted upstream					
7	from the target transceiver via the mixed channel, the transmitter configured to reduce					
8 -	downstream transmission power in response to determining that echo from downstream					
9	transmissions interferes with upstream transmissions from the target transceiver.					
1	25. The transceiver of claim 24, wherein the transmitter is configured to lowe					
2	the downstream transmission power in response to determining that the power level					
3	causes an above-threshold noise level in upstream transmissions from the target receiver.					
	F					
1	26. The transceiver of claim 24, wherein the transmitter is configured to set					
2	the power level in part by selecting a signal constellation for transmitting data					
3	downstream.					
1	27. The transceiver of claim 24, wherein the transmitter includes a					
-	2 The dament of elamin 27, which the dament the distinction					

probabilistic trellis encoder to perform the encoding.

1	28.	The transceiver of claim 24, wherein the receiver includes a Viterbi
2	decoder and is	s configured to scale 1D branch metrics used in the decoder in a manner
3	responsive to	reliability information derived from the received digital data.

- 29. A receiver capable of receiving signals from a mixed channel that includes a digital network and an analog channel serially connected to the digital network, the receiver comprising:
- a Viterbi decoder configured to process the received signals in a manner responsive to reliability information derived from the signals, the reliability information being indicative of potential magnitudes of quantization noise in the received signals.
  - 30. The receiver of claim 29, further comprising:
- an adder configured to subtract an estimate of an echo level from each received signal and to transmit the echo-subtracted signal to the Viterbi decoder; and
  - a device configured to derive the reliability information from the received signals;

5 and

6

7

1

2

3

4

1

2

3

1

- wherein the adder and device are coupled to obtain the received signals in parallel.
- 31. The receiver of claim 29, wherein the received signals are representative of signal points belonging to constellations, and the reliability information corresponding to ones of the received signals is a function of minimum distances of the constellations associated with the ones of the received signals.
- 32. The receiver of claim 29, wherein the Viterbi decoder is configured to scale 1D branch metrics that are used in decoding in a manner that is responsive to the reliability information.